

Top View

Vcc between 4.5 and 16 volts (TLC555 - between 2.0 and 15 V)

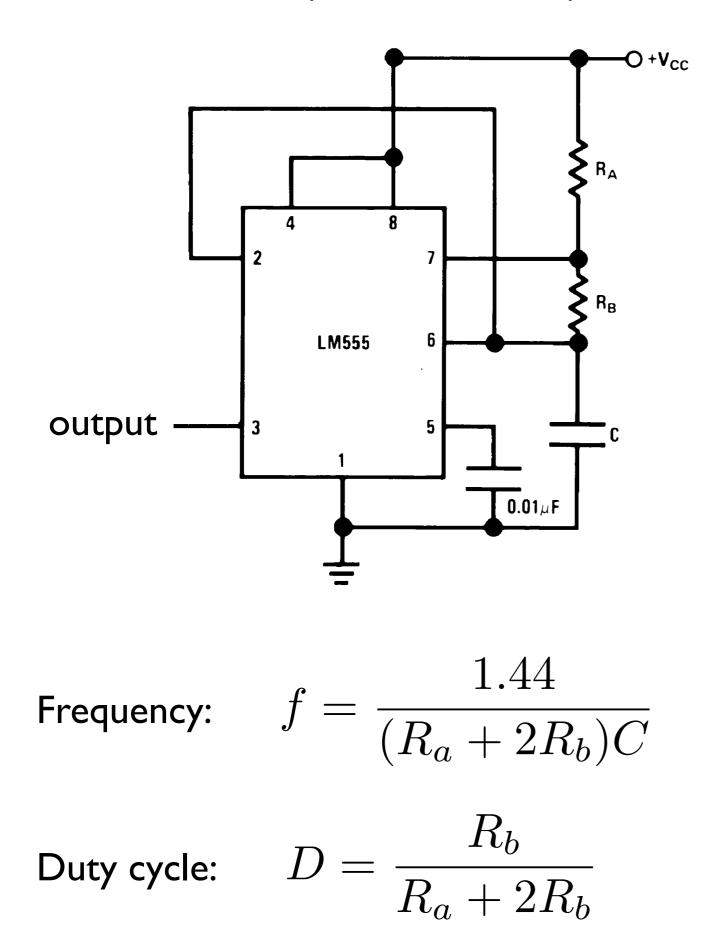
timing from microseconds to hours

monostable (single pulse) mode

astable (pulse train) mode

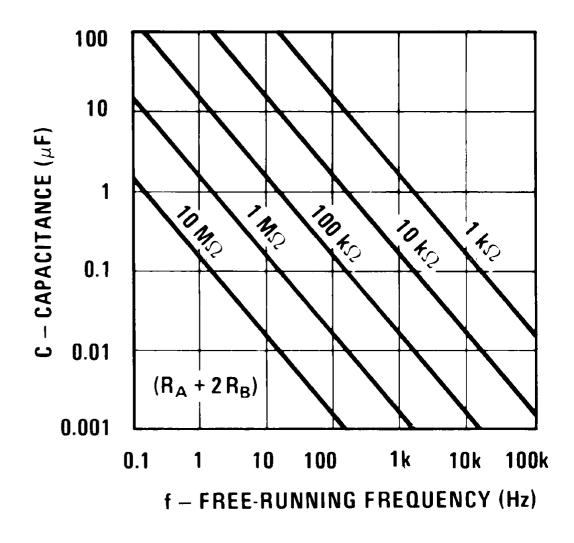
use power bypass caps (. I uF ceramic in parallel with I uF electrolytic)



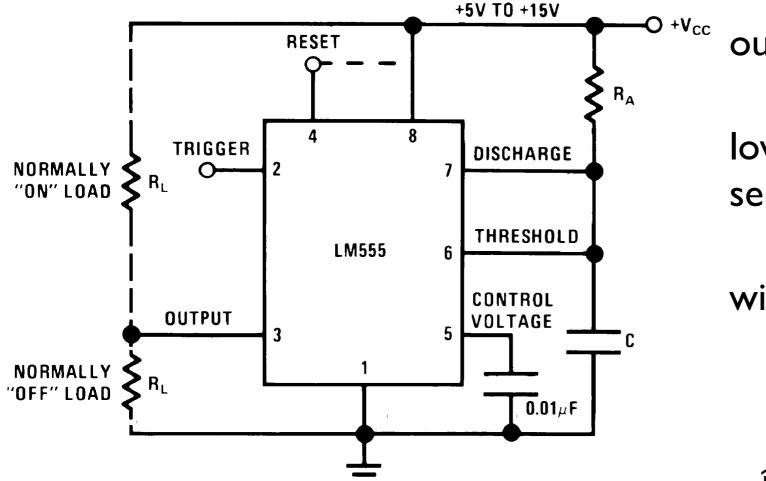


free running (self-triggering)

frequency and duty cycle set by external Ra, Rb, and C



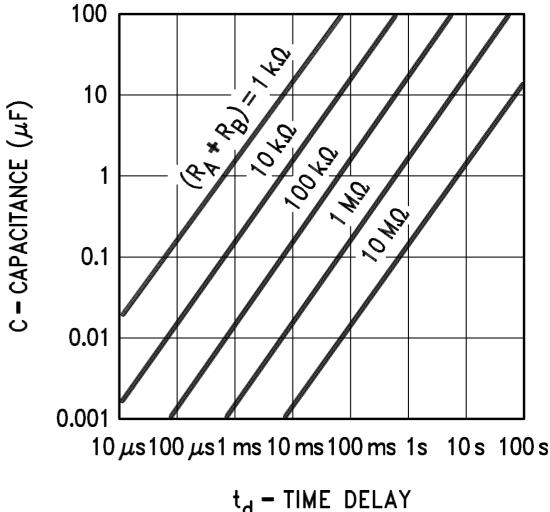
LM555 monostable mode



output is stable low

low-going trigger on 2 sends the output high

will return low based on $R_{\!\scriptscriptstyle A}$ and C



note: the trigger should be returned high before the end of the timing cycle